

**AMENDMENTS TO THE CLAIMS**

Please cancel claims 2-11 and 14-20, and amend claims 1, 12 and 13, as shown in the following list of claims.

1. (Currently Amended) An integrated circuit on a chip, comprising:  
a substrate; ~~and~~  
at least one scan chain disposed in the substrate, with scan cells connected to form a series chain, each connection being formed according to a layout constraint with a minimum dimension provided by design rules for an assigned routing ~~layer~~; layer; and  
an auxiliary routing net positioned in parallel beside the scan chain,  
wherein the auxiliary routing net has metal lines of different lengths, and the auxiliary routing net does not function when the scan chain is originally formed.
- 2.-11. (Canceled)
12. (Currently Amended) The integrated circuit as claimed in claim ~~11~~ 1, wherein one of the metal lines has sufficient length to replace one of the connections in the scan chain.
13. (Currently Amended) The integrated circuit as claimed in claim ~~11~~ 1, wherein one of the metal lines has a length equal to at least two scan cells in the scan chain.
- 14.-20. (Canceled)